# Aries ATU (Arduino Nano 33 IoT)

This document applies to the most recent version of Aries using an Arduino Nano 33 IoT processor and using a Nextion display. The display is optional and does offer power and VSWR displays if wanted. ARIES can be used in two ways:

* In normal mode, is it controlled by Thetis to select its TX frequency and TX and RX antennas. TUNE is initiated by CAT commands directly by Thetis. A specific CAT port is used, because Thetis pushes information direct to Aries.
* In “standalone” mode is uses a normal CAT port, and polls for TX frequency every few seconds. TUNE is initiated by hardwired connection. This mode is intended for ARIES being installed in a QRO amplifier attached to an Andromeda radio that already has an ARIES built into it. In standalone mode Aries has no knowledge of the antennas in use.

There is also an “amplifier protection” mode which is optionally included into the source code and which requires a display. This mode controls “tripping” of amplifier PTT if a fault condition (eg high VSWR, excess forward power, over temperature…) occurs. A display page will show the trip condition and allow manual reset once the fault has been cleared.

# Aries Concept

The concept is for an ATU to be installed into Andromeda. Kjell suggested the standard L-C architecture.

## Rationale

|  |  |
| --- | --- |
| **Aries Idea** | **Why** |
| Aries is controlled by Thetis using CAT commands | Provides tighter user interface and display through Thetis of tuner status |
| Aries is connected to the radio TX path, not in the RX path | We do not need to switch the ATU out of circuit for RX. |
| Aries will use the well known “L match” arrangement with 8 inductors and 8 capacitors | It works, and appropriate component values are well known |
| Aries stores L/C tuning solutions with a “grid” of 10KHz steps | Fine enough grid for most antennas (but possibly not magnetic loops) |
| Aries stores separate solutions for each of the 3 antenna outputs | So that 3 different antennas can be connected, with separate tuning for each |
| Aries is sent the tuned TX frequency by CAT command.  A new command is sent every time the frequency moves to a new frequency step (~10KHz) | So that it “knows” frequency as the radio is tuned and can give feedback straightaway that a stored solution is or isn’t available.  Also so that it doesn’t have to measure TX frequency for an SSB signal, which would be problematic. |
| Aries measures VSWR itself | It needs a new VSWR measurement every 10-16ms; it would be too slow to send VSWR reports from Thetis during tuning. |
| Aries can be user selected to be active or bypassed for each antenna individually | To allow it to be switched out if ANT3 is connected to an external linear, for example |
| There needs to be a way for the user to tell Aries to clear the Tuning solutions for any of the 3 antennas individually | So that if an antenna is changed or modified, Aries doesn’t try to use “old” solutions |
| Aries searches for a new tuning solution when TUNE is selected on the radio |  |
| An encoder is available to “tweak” ATU tune results | Encoder button long press: toggle coarse/fine tune (L/C step size)  Encoder button press: toggle between inductance & capacitance |

## Interface to Thetis

|  |  |
| --- | --- |
| **THETIS action** | **Aries action** |
| At Startup:  Find the band and hence antenna in use.  Find if Aries enabled for that band. Send enabled/not enabled message.  For initial TX frequency: send frequency.  Initialise ATU display symbol – either “off” or “no solution” |  |
| When an antenna is changed, Thetis sends an enable or bypass command to Aries and new antenna number  If Aries enabled, Thetis displays a symbol on the display  If a tuning solution is reported as available, the green LED is lit and the ATU display symbol is highlighted. | Aries searches for a tuning solution: on the frequency specified, then up to ±50KHz away. If it finds a solution it selects it ready for use; it not it selects “bypass”. Note the relays are not changed – it just prepares to set these relay values.  A message is sent back to Thetis with solution available or not available |
| When a band is changed, Thetis sends a new frequency message to Aries  When the radio is tuned by more than 10KHz from the last frequency, a new frequency message is sent to Aries.  If a tuning solution is reported as available, the green LED is lit and the ATU display symbol is highlighted. | If enabled: Aries searches for a tuning solution: on the frequency specified, then up to ±50KHz away. If it finds a solution it selects it ready for use; it not it selects “bypass”. Note the relays are not changed – it just prepares to set these relay values.  A message is sent back to Thetis with solution available or not available. |
| When TX is initiated | If the frequency has been changed, Aries drives the new relay selection for the new tuning solution from memory to the relays. If no solution was available, it selects bypass.  **This needs to happen quickly – driven by PTT** |
| When TX removed | The relays are left in the same position ready for the next operation |
| When TUNE is selected: the red LED is lit. A “tune now” message sent to Aries.  Thetis stays in the TUNE state, and indicates success/no success using the green LED and display symbol highlight. (?option to stay in for fine tune, or just exit) | Aries begins its algorithm to find a new solution. When complete, if a good solution was found it is stored in EEPROM.  Aries sends a message back saying “tune complete” and “successful/not successful”. |
| If user requests settings for ANT1/2/3 to be cleared  Display “erasing…”  Change display to “Done” | All tuning solutions for that antenna are erased from EEPROM. If that antenna is selected, Aries enters bypass state. (note this takes around 5 seconds I think)  ARIES sends a response message when complete If selected antenna == erased antenna, ARIES sends “not successful” |



Erasing….Done

H/W 2; S/W 17

CAT port

Com4

Solutions

X

3

2

Erase

Erase

Erase

Ant

ATU enabled

X

X

1

Enabled

f/w version

X

ATU

Figure 1: Suggested setup tab



ATU

Figure 2: Andromeda display having an “ATU” symbol:

# Operating Modes

There are 2 fundamental operating modes of the ATU:

Normal Mode

* Aries in installed inside a transceiver (eg Andromeda) or as an external add-on
* Its CAT port must be set in the Aries CAT port in the Thetis Andromeda settings
* No Nextion display is used.

Standalone Mode

* Standalone mode is detected by a jumper installed in J17.
* In this mode Aries is installed in an add-on high power amplifier
* Its CAT port must be selected from the “normal” CAT1-CAT4 ports in Thetis NOT the Aries CAT port
* A Nextion display is used and must be connected
* The antenna input is selected by hardwired inputs
* TUNE is controlled by a hardwired input to J6.
* Aries polls for TX frequency using a CAT command.
* A setup form can be accessed from the “Engineering” display page. From here the power meter max power scale setting and antenna solution erase (eg if antenna changed) can be accessed. The forward/reverse power divider resistors must be set correctly for the intended power scale.
* Amplifier protection is selected automatically if an MCP23017 is detected. This connects via J1.

The standalone code “deltas” will be:

1. When in standalone, send a frequency request CAT command every 5 seconds
2. Take antenna change from 2 h/w inputs D2 & D5 (document)
3. Take ATU enable/disable from Nextion display.
4. Save ATU enabled/disabled to EEPROM
5. Put back code for h/w TUNE input
6. Design suitable displays
7. Save current display to EEPROM in standalone mode
8. Save current display scale to EEPROM in standalone mode
9. For each selected display scale, we will need an ADC scale factor to compute power.
10. There will need to be a way to erase memorised ATU settings for each antenna.
11. Connect an amplifier protection board through an additional I2C chip to allow monitoring of fault conditions in the PA.
12. After power up, check the fault conditions and if no fault, reset a flip flop on the protection board.
13. Additional display screen if there is a “trip” for excessive VSWR or reverse power.
14. RESET button on the additional display screen.
15. Display band information from the protection board.

Standalone mode is detected by presence of a jumper on J17.

As an add-on to Standalone mode, an amplifier protection function has been added. This is auto-enabled by detecting the presence of an MCP23017 on the I2C bus.

# ATU and the Smith Chart

The Smith Chart is a plot of the complex reflection coefficient S11on an Argand diagram. It is a useful way to visualise what a tuner needs to do. On the one diagram you see:

* Centre dot – perfect match; load impedance = Zo
* Left hand dot – short circuit
* Right hand dot – open circuit

|  |  |
| --- | --- |
|  |  |
| Complex impedance plot | Complex admittance plot (horizontal mirror image) |

It is then possible to plot how inductance and capacitance values move the impedance of a load. A series inductance will move clockwise along a line of constant resistance. A shunt capacitance will move clockwise along a line of constant admittance.

|  |  |
| --- | --- |
|  |  |

Figure 3: Series Inductance and Shunt Capacitance effects

By adding L and C appropriately, the L match network can match any impedance on the diagram. But the series L, shunt C can only achieve one solution, and whether the capacitor is at the input or output depends on where the load impedance is placed. You can then plot regions where the L-match ATU can match – the grey regions cannot be matched:

|  |  |
| --- | --- |
|  |  |
|  |  |

Figure 4: Matching Regions for the 2 Forms of the L-Match ATU

# Matching network

The matching network is provided by 8 inductors and 8 capacitors in a classic L match arrangement. If the capacitor is connected to the input end, it matches low impedance loads; if to the output, it matches high impedance loads.



1st bench model: capacitance steps in 10pF steps up to total 2800pF; inductance stepped 40nH steps to total 10uH. Tuner could match 8:1 low impedance and 8:1 high impedance resistive loads at 1.9MHz.

Version 4 hardware constructed using: The following values:

|  |  |  |  |
| --- | --- | --- | --- |
| component | Target | **value used (pF)** | combined value (pF) |
| C33, C34 | 21.1 | 20 | 10 |
| C35, C36 | 42.2 | 39 | 19.5 |
| C38, C38 | 84.4 | 82 | 41 |
| C39, C40 | 168.8 | 180 | 90 |
| C41, C42 | 337.5 | 330 | 165 |
| C43, C44 | 675 | 680 | 340 |
| C45, C56 | 1350 | 1300 | 650 |
| C57, C58 | 2700 | 2700 | 1350 |

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| target inductance uH | Toroid | Al | Calc Turns | Actual turns | Achieved inductance uH |
| 0.023 | 0.3” dia air wound. 1 turn 0.15” length | | | | |
| 0.047 | 0.3” dia air wound. 3 turns 0.2” length | | | | |
| 0.094 | T94-6 | 70 | 3.7 | 4 | 0.11 |
| 0.188 | T94-6 | 70 | 5.2 | 5 | 0.18 |
| 0.375 | T94-2 | 84 | 6.7 | 7 | 0.41 |
| 0.75 | T94-2 | 84 | 9.4 | 9 | 0.68 |
| 1.5 | T94-2 | 84 | 13.4 | 13 | 1.42 |
| 3 | T94-2 | 84 | 18.9 | 19 | 3.03 |

In both cases: the smallest value selected by bit 0; component is selected in-circuit if the data bit is set to 1.

The relay to switch between low and high Z will need to be driven by a processor pin directly.

# CAT messages required

Re-use existing CAT messages where possible

|  |  |  |
| --- | --- | --- |
| **Event** | **Message** |  |
| TX on/off | Signalled by hardwired signal |  |
| TUNE on/off | CAT message ZZTUn;  Sent from PC to Aries | n=0: no tune; n=1: TUN active |
| Frequency change | CAT message ZZFTmmmmmmmmmmm;  Sent from PC to Aries | mmmmmmmmmmm: 11 digit frequency (Hz)  eg 00014320000 = 14.32 MHz  eg 00001850000 = 1.85 MHz  (expected to be steps of 10KHz) |
| TX Antenna change | CAT message ZZOCn;  Sent from PC to Aries. | n=1: Ant1; n=2: Ant2; n=3: Ant3 |
| RX Antenna change | CAT message ZZOAn;  Sent from PC to Aries. | n=1: Ant1; n=2: Ant2; n=3: Ant3 |
| Erase Solution | CAT message ZZOZn;  Sent from PC to Aries.  Response: ZZOZn;  Sent by Aries to PC | n=1: erase solutions for Ant1; n=2: erase for Ant2; n=3: erase for Ant3 |
| Fine tune L/C | CAT message: ZZZEnnm;  Sent from PC to Aries. | nn= encoder number and direction. m= number of steps  Allowed values: 01=L c/w; 02=C c/w; 51=L ac/w; 52=C ac/w |
| ATU success/fail | CAT message: ZZOXn;  Sent by Aries to PC | n= 0: no ATU solution found; n=1: suitable tuning solution found. |
| ATU Enable | CAT message: ZZOVn;  Sent from PC to Aries | n=0: ATU inactive; n=1: ATU active, and will tune on demand |
| Query s/w Version | ZZZS;  Response ZZZSppnnmmm; | pp=product id  1: Andromeda 2: Aries 3: Ganymede  nn= hardware version  mmm= s/w version |
| Set ATU Options | ZZOYn; | n =0: always full tune  n=1: allow quick tune |

# Processor Issues

The processor does not need to be powerful; it will be idle most of the time waiting for relays to settle. It does need enough memory to hold the tune solutions for one antenna.

An Arduino Nano 33 IoT seems suitable. This has a SAMD21G18A processor.

|  |  |
| --- | --- |
|  |  |

## Timers

Previous projects suggest I should know what timers are already used! The Arduino time functions (eh milis() seem to use the processor “systick” timer. From the variant.cpp file:

TCC0, TCC1, TCC2, TC3, TC4, TC5 can all be used for PWM. TC5 used for PWM on Dig3

I will need a timer tick interrupt. There is an Arduino Zero timer library Github.com/EHbtj/ZeroTimer seems to use TC3 and/or TCC0. The timer period is specified in microseconds.

## Hardware I/O

Relays will be SPI driven using 3x TPIC6B595 shift register.

## I/O Pin assignment

|  |  |  |
| --- | --- | --- |
| **Function** | **Pin** | **Comment** |
| Serial | TX/DIG0  RX/DIG1 | Allows use of Nextion touchscreen I/O |
| SPI | D13/SCK  D11/MOSI | Serial clock and data to TPIC6B595 |
| I2C | A4 / SDA  A5 / SCL | For EEPROM (and MCP23017 on amplifier protection board in standalone mode) |
| LOAD | DIG6 | Rising edge loads serial data from SPI |
| High/Low Z Relay  (Rev 4 & below H/W) | DIG8 | 1=high Z; 0=low Z |
| Amplifier Protection Trip Detect (Rev 5 HW) | DIG8 | Input from S/R flip flop.  =1: Normal operation  =0: tripped. |
| PTT | DIG10 | 0=TX; 1= RX. Needs pullup. Interrupt driven. |
| VSWR Bridge inputs | A0, A1 | A0=fwd; A1=rev. We will need to scale the analogue voltage for 3.3V max |
| External TUNE input | DIG9 | Needs pullup & interrupt. 0=TUNE; 1=no tune. |
| Status LED | A7 | 1Hz blink |
| Tune LED | DIG7 | Lit if tuned |
| T/R (PTT) output | DIG3 | =1: PTT asserted (TX) |
| L/C adjust Encoder | A2, A3 |  |
| Encoder pushbutton | DIG4 |  |
| Antenna Input (standalone mode) | DIG5, DIG2 |  |
| Standalone mode Select | DIG12 | =0 at powerup: standalone mode.  =1: normal mode.  Note external pullup resistor required this is part of the SPI pins, but it can be read) |
| PA Current Monitor | A6 | Analogue input |
| Unused | A7 |  |

Antenna input in standalone mode: Note these need a series diode on the PCB.

|  |  |  |  |
| --- | --- | --- | --- |
| **Antenna** | **Input B**  **D5** | **Input A**  **D2** | **~D5,~D2** |
| 1 | H | H | 00 |
| 2 | H | L | 01 |
| 3 | L | H | 10 |
| 4 | L | L | 11 |

I/O pin changes on Rev 5 hardware:

* ENC 2 removed.
* Jumper on D12 to select between STANDALONE and NORMAL modes
* High/Low Z select moved from DIG8 to IC7,D3
* Antenna inputs added on DIG2, DIG5
* Amplifier protect reset output on DIG8
* PA current monitor on A6
* A7 is currently spare.

## I2C Device Assignment

|  |  |
| --- | --- |
| EEPROM | 0x50, 0x51 (includes A16 as bottom bit) |
| MCP23017 (protection mode) | 0x20 (presence auto detected) |

# EEPROM I2C Interface

An external EEPROM will be needed. We need 3 bytes per frequency to store tuning solutions. If we store a solution per 10KHz, we need 100 settings per MHz ie approx. 6000 settings for the HF band ie 18 KByte. If we have 3 antennas and separate solutions for each, that’s 54Kbyte ie near 500Kbit. 2Mbit+ EEPROMs are readily available with I2C interface. The Microchip 1Mbit EEPROM (24FC1026-I/P) will be suitable and an Arduino library is available. FC devices can clock at 1MHz. The EEPROM connects to the I2C interface.

## EEPROM Data access

Data for an entire antenna will be read into SRAM when the antenna is changed. There was a potential race issue with the EEPROM. This can be fully avoided now a Nextion display is used for debug.

To write back individual solutions it may make sense to use individual byte writes. To erase an antenna worth of settings, use block write (128 bytes take the same 5ms). To read data, use sequential read with one address transaction (~40us) then individual reads transferring one new byte only (10us).

For convenience the “no solution” state should be the shipped condition –solution = 0xFFFFFF.

Each block of data for each antenna should begin at a page boundary (128 bytes) and be sized so that a fixed number of page writes can be made to erase it. That suggests it should be a little bigger than needed.

There is an Arduino library (extEEPROM) that supports the 24FC1026 device.

# SPI Interface

Serial data will be shifted into two TPIC6B595 shift registers. A third will be provisioned for an add-on board to drive a T/R relay and 3 antenna select relays: this would allow standalone operation with older HPSDR radios. Byte 1 is the first shifted, reaching the end of the SR chain.

|  |  |
| --- | --- |
| Byte | Meaning |
| 1 | Bit 0: antenna select 1. 0 = antenna selected.  Bits 2:1: antenna select 3-2. 1 = antenna selected.  On rev 5 PCB: Bit 3 = High/LowZ. 1= High Z |
| 2 | Capacitance values. Bit 0 = smallest. |
| 3 | Inductance values. Bit 0 = smallest. |

RX and TX relay settingss could be different. In most cases this register will be written whenever there is a T/R change. However any change of RX antenna should be sent to the hardware immediately unless TX is already in progress.

SPI data to be written whenever PTT changes state, when RX antenna changed, or when new TUNE solution sent out during tune. There are 2 potential race conditions

1. If RX antenna data is already being written when PTT asserted, the data will need to be written again when the shift ends. A flag will need to be set.
2. If TX strobe deasserted while tune solution being written out during the tune algorithm

# PTT

When PTT pressed you get several “press” events. This could be because of the presence of RF, moving the ground while the signal passes through the threshold. Suggest instead:

1. Set PTT from interrupt;
2. Set 32 ms “min PTT duration” timer;
3. Poll PTT to see if it should be released
4. When released, set 16ms “min PTT inactive” timer

# VSWR Bridge

Currently using a Stockton Bridge with a binocular core ferrite. This requires no “balance” adjustment and gives a calibrated power measurement.

Calculated (see notebook) that RMS line voltage = 0.0837N where N = Arduino ADC reading.

(From the Stockton bridge spreadsheet: 3.3V input corresponds to 147W, ie 85.7Vrms

ADC reading N = 1024\*Vin/3.3 Therefore Vrms=85.7N/1024 = 0.0837N)

Power = Vrms2/50; VSWR = (Vf+Vr)/(Vf-Vr)

# Stored Tune Solution Data Structures

Proposed approach: 3 blocks of EEPROM memory - one for each antenna.

Solutions are stored every 10KHz, starting at 0; each antenna stores solutions for all frequencies from 0 to 61.49MHz (just over the Nyquist rate for HPSDR radios). Solutions numbered 0-6149; total count 6150

There are 3 bytes per solution stored:

|  |  |
| --- | --- |
| **byte** | **Meaning** |
| 0 | bit0=1: no data; bit0=0: data OK  bit7=1: high Z; bit7=0: low Z |
| 1 | Inductance word |
| 2 | capacitance word |

Address of solution in EEPROM, with 1st antenna = antenna 1:

(Antenna-1)\*32768 + 3\*Int(Freq\_in\_KHz/10KHz)

Hence for antenna 2 at 61.471MHz, the solution will be at address 57356 decimal

We need an EEPROM storing at least 128Kbyte.

For the standalone version with Nextion display 3 additional variables are stored:

|  |  |
| --- | --- |
| **address** | **meaning** |
| 0x1FFF0 | Display page in use. Legal values 1-4. If other value, use page 4 |
| 0x1FFF1 | Peak/average display. Bool. |
| 0x1FFF2 | ATU Enabled/disabled. Bool. |
| 0x1FFF3 | Display scale. 0=100W; 1=200W; 2=500W; 3=1000W; 4=2000W |
| 0x1FFF4 | Allow quick tune. !=0 to allow quick tune. |

## Local data / data structures

Proposed approach: store solutions for whole HF band in SRAM for one antenna. If the antenna changes, load a different block from EEPROM.

* 1. Storage required ~18Kbyte. Impractical with 8 bit AVR, still needs Nano 33
  2. Readout a new batch whenever the antenna changed
  3. Readout time ~165ms; means there is a significant period when TX not possible

|  |  |  |
| --- | --- | --- |
| unsigned int | GTunedFrequency10 | Frequency the ATU is tuned to, in units of 10KHz |
| bool | GATUEnabled |  |
| unsigned int | GQueuedCATFrequency | 0xFFFF: no frequency stored  0-6149: ATU frequency passed by Thetis during TX |
| byte | GAntenna | Antenna number (1-3) =0 if not set |
| bool | GFrequencySet | True if a frequency has been set by THETIS |
| bool | GPCTuneActive | If true, a TUNE is happening |
| bool | GPTTPressed | If true, PTT is pressed |
| bool | GTXAllowed | True if ATU solution should be sent when TX asserted |
| bool | GATUIsTuned | True if valid solution already set |
| byte\* | GSolutionBuffer | Block of tuning solutions buffered in internal memory  This holds full data for one antenna |



Figure 6: EEPROM Memory map

# External Aries Version

As well as internal to the Andromeda radio, we could use Aries as an external ATU as an add-on for existing radios. We would need to modify Thetis to route RF always through ANT1, and have an external T/R switch and 2 ANT select relays.



An additional 4 bits are driven to the SPI shift register to control these relays.

# Software Algorithms

## Event Response

|  |  |
| --- | --- |
| **Event** | **Action Required** |
| When TX asserted | If existing solution already driven: no action  If new solution ready to go: drive solution to relays  If “Tune in progress” set: commence tune, after “TT on” delay  Set “TX in progress” flag |
| When TX deasserted | Clear “TX in progress” flag  Clear “tune in progress” flag  If Tune algorithm in progress, terminate the algorithm |
| When new frequency received | If already in TX – wait until TX completed  Select closest solution or bypass  Send CAT message with solution available/not available |
| When new TX antenna received | Temporarily set bypass “null solution”  Read new block of data from EEPROM  Check tuning solutions for antenna N around current frequency  Select closest solution or bypass  Send CAT message with solution available/not available |
| When ATU enable received | Treat as a new antenna |
| When ATU Disable received | Drive “bypass” setting to ATU  Set flag |
| When “TUNE start” message received | Set “tune in progress” flag  If TX already asserted, begin algorithm |
| When “TUNE end” message received | Ignore – this is signalled by removal of PTT |
| When TUNE algorithm complete | Store solution to EEPROM  Report success after store complete |
| If L/C fine tune CAT message received | Adjust L/C setting  Do not re-store |
| When erase received for ant N | Erase the block of data for that antenna  If same antenna currently selected:  set solution to use = bypass  clear RAM copy of tuning solutions  Send response message |

## Original Search Algorithm

Do the following while GTuneActive is true:

Quick tune:

1. Try “fine step L and C” around current setting
2. If that fails select full tune

Full Tune:

1. On the basis of frequency, select min/max L and C and coarse, mid step size
2. Select “Low Z”
3. Step through C values at coarse step, find step with min VSWR & achieved VSWR
4. Select “High Z”
5. Step through L values at coarse step, find step with min VSWR & achieved VSWR
6. If best result is low Z:
   1. Re-select Low Z
   2. Select best C value
   3. Step through L values at coarse step, find step with min VSWR & achieved VSWR
   4. Select best L value
   5. Step through C values +/- 2 coarse steps at mid step for min VSWR
   6. Select best C value
   7. Step through L values +/- 2 coarse steps at mid step for min VSWR
   8. Select best L value
   9. Step through C values +/- 2 mid steps at step=1 for min VSWR
   10. Select best C value
   11. Step through L values +/- 2 mid steps at step=1 for min VSWR
   12. Select best L value
7. Else if best result was high Z
   1. Select best L value
   2. Step through C values at coarse step, find step with min VSWR & achieved VSWR
   3. Select best C value
   4. Step through L values +/- 2 coarse steps at mid step for min VSWR
   5. Select best L value
   6. Step through C values +/- 2 coarse steps at mid step for min VSWR
   7. Select best C value
   8. Step through L values +/- 2 mid steps at step=1 for min VSWR
   9. Select best L value
   10. Step through C values +/- 2 mid steps at step=1 for min VSWR
   11. Select best C value
8. Store result to EEPROM if final VSWR < 1.5
9. Report success/fail and end



At each tick:

1. Check if “terminate” signal
   1. Exit if so with no result saved
2. Measure VSWR
3. If VSWR < min already achieved
   1. Record step and VSWR
4. If next step > end:
   1. move to next state
5. Else
   1. set next step
   2. drive solution

To find next candidate solution:

If (setting == end)

Signal end;

Else

New setting = constrain (setting+step, min, max);

The current algorithm executes potentially 144 search steps:

* Initial Quick tune:
  + 16 fine steps L
  + 16 fine steps C
* Full tune, if quick fails:
  + 16 coarse C, low impedance
  + 16 coarse L, high impedance
  + 16 coarse L or C depending on best match
  + 16 mid step L
  + 16 mid step C
  + 16 fine step L
  + 16 fine step C
* Note that:
  + For low frequencies we can accept bigger steps
  + For high frequencies we can limit the search space

## New Search Algorithm

With real antennas the algorithm does not always find a match, or the best match. The “narrow down” process seems OK but the initial search isn’t comprehensive.

|  |  |
| --- | --- |
| Current |  |
| Required |  |
| And Potentially |  |
| And for some loads |  |

Suggested approach:

* Keep the 2 stage algorithm
* Have each part table driven, so the search range can be set by frequency
* For the first part, have the number of search steps set by the table
* Potentially that might allow user settable search algorithms to trade speed & performance
* Stage 1a: perform sweeps of one parameter according to table
* Stage 1b: for the minimum found, perform a sweep of the opposite parameter
* Stage 2a: medium sweep 1st parameter
* Stage 2b: medium sweep 2nd parameter
* Stage 2c: fine sweep 1st parameter
* Stage 2b: fine sweep 2nd parameter
* In all cases you swap over the parameter being swept at each stage
  + 1st table:
    - Upper frequency
    - Stage 1a Start Row for this frequency
    - Number of rows for this frequency
    - Max L, Max C
    - Stage 1b min, max, step
    - Stage 2 mid step +/- range, step size
    - Stage 2 fine step +/- range
    - (the entire algorithm can be changed by replacing this table with no code change)
  + 2nd table with Parameters for stage 1a:
    - High/Low Z
    - Sweeping L/C
    - Fixed value
    - Min, max, step value
* To be recorded in 1st stage, to find min
  + C value
  + L value
  + High/Low Z value
  + VSWR value
  + (it doesn’t matter which sweep we find it on)



# Testing

## Test Loads

|  |  |  |  |
| --- | --- | --- | --- |
| Switch position | VSWR | Resistance | Construction |
| 1 | 8:1 Low | 6.25R | 16x100R parallel |
| 2 | 5:1 Low | 10R | 10x100R parallel |
| 3 | 4:1 Low | 12.5R | 8x100R parallel |
| 4 | 3:1 Low | 16.6R | 6x100R parallel |
| 5 | 2:1 Low | 25R | 4x100R parallel |
| 6 | 1:1 | 50R | 4x100R parallel, in series with 4 more 100R parallel |
| 7 | 2:1 High | 100R | 2x100R parallel, in series with 2 more 100R parallel |
| 8 | 3:1 High | 150R | 3x100R series, parallel with another 3x100R series |
| 9 | 4:1 High | 200R | 2x100R series  (4x100R in series, parallel with 4x100R series) |
| 10 | 5:1 High | 250R | 2x100R series, +2x100R parallel |
| 11 | 8:1 High | 400R | 4x100R series |

These need to be good for around 5W

# Amplifier Protection

As well as providing a standalone display, there is a desire to merge a simple amplifier protection function. This will enable Aries to be part of a higher PA, connected to Thetis but with its antenna controls not driven by Thetis. It will detect fault conditions (high VSWR, high reverse power…) and trip the PA near instantly using a simple hardware flip flop circuit; it will allow the Arduino to monitor the fault conditions and show a “tripped” display when they have happened. J1 connects Aries to the protection board.

The protection board has comparators for its analogue inputs. Any of those going above threshold results in the comparator output going low. All 4 outputs are ANDED together; any one above threshold makes the AND gate go low and trip an S/R flip flop. The flip flop output being low removes amplifier PTT and will be monitored by the Arduino (DIG8). The flip flop can be reset by asserting the Reset signal high then low (also needed after power up).

* U1.A detects an “indicative” VSWR (it isn’t accurate);
* U1.B detects excess reverse power;
* U1.C detects excess driver power;
* U1.D (optional) detects excess temperature.

Digital signals used for the protection circuit come from an MCP23017 on the I2C bus. Also DIG8 allows monitoring of the flip flop state. When a trip occurs, a new Nextion display page should be shown. That will poll the 4 fault condition inputs periodically, and when OK enable a “reset” button. When reset is pressed, the Flip flop reset should be cycled and normal operation resumed.

The MCP23017 signals are as follows:

|  |  |  |
| --- | --- | --- |
| **Pin** | **State** | **Description** |
| GPB0 | Input | VSWR comparator input. 0 if tripped |
| GPB1 | Input | Reverse power comparator input. 0 if tripped |
| GPB2 | Input | Drive power comparator input. 0 if tripped |
| GPB3 | Input | PA Temperature comparator input. 0 if tripped |
| PB4 | Input | unused |
| GPB5 | Input | unused |
| GPB6 | Input | unused |
| GPB7 | Input | Low if 6M band selected. Display “6M” |
| GPA0 | Output | Protection Flip Flop Reset. =0: normal state; =1: applies reset |
| GPA1 | Output | PA PTT enable. =1: normal operation. =0: PTT disallowed. |
| GPA2 | Input | Low if 10M band selected. Display “10M” |
| GPA3 | Input | Low if 15M band selected. Display “17+15M” |
| GPA4 | Input | Low if 20M band selected. Display “30+20M” |
| GPA5 | Input | Low if 40M band selected. Display “60+40M” |
| GPA6 | Input | Low if 80M band selected. Display “80M” |
| GPA7 | Input | Low if 160M band selected. Display “160M” |

MCP23017 base address = 0x20. MCP23017 registers are accessed in the mode where IOCON.BANK=0:

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Register** | **Address** | **Purpose** | **Required State** | **Initial Value** |
| IODIRA | 0x00 | I/O direction. Per bit; 1 = input | 0,1=o/p; 2-7 = i/p | 0xFC |
| IODIRB | 0x01 | I/O direction. Per bit; 1 = input | All inputs | 0xFF |
| IOPOLA | 0x02 | I/O polarity. 0 = not inverted | All not inverted | 0x00 |
| IOPOLB | 0x03 | I/O polarity. 0 = not inverted | All not inverted | 0x00 |
| GPINTENA | 0x04 | Interrupt on change. 1 = enabled | No interrupts | 0x00 |
| GPINTENB | 0x05 | Interrupt on change. 1 = enabled | No interrupts | 0x00 |
| DEFVALA | 0x06 | Interrupt compare. Per bit | Don’t care | 0x00 |
| DEFVALB | 0x07 | Interrupt compare. Per bit | Don’t care | 0x00 |
| INTCONA | 0x08 | Interrupt on change control. Per bit. | Don’t care | 0x00 |
| INTCONB | 0x09 | Interrupt on change control. Per bit. | Don’t care | 0x00 |
| IOCON | 0x0A | I/O configuration | Bank=0; mirror=0;  Seqop=0; DISSLW=1; ODR=1; INTPOL=0; | 0x14 |
| IOCON | 0x0B | Same register |  | Ignore |
| GPPUA | 0x0C | Pullup enable. 1 = enabled; per bit | All 1 | 0xFF |
| GPPUB | 0x0D | Pullup enable. 1 = enabled; per bit | All 1 | 0xFF |
| INTFA | 0x0E | Interrupt flag; readonly |  | n/a |
| INTFB | 0x0F | Interrupt flag; readonly |  | n/a |
| INTCAPA | 0x10 | Interrupt capture; readonly |  | n/a |
| INTCAPB | Ox11 | Interrupt capture; readonly |  | n/a |
| GPIOA | 0x12 | GPIO read register | b1=0 (disables PTT) | R/W |
| GPIOB | 0x13 | GPIO read register |  | R/W |
| OLATA | 0x14 | GPIO write register |  | 0x00 |
| OLATB | 0x15 | GPIO write register |  | 0x00 |

When amplifier protection is provided, there are two analogue inputs (A6 and A7). A6 measures PA current; A7 is not used.

Kjell’s figures: Input = 0 => current = 0A; Input = 3.12V => current = 20A

Therefore full scale ADC reading = 21.154A. Current = 21.154N/4096 = 0.00051645N

Potential conflicts with use of I2C bus:

* Erase – essentially the process just waits until the erase complete, ignoring normal processing. So no conflict.
* Read block – this happens in a single event; the processor may miss a few ticks. No conflict.
* Write solution – this happens in a single event. No conflict.

The protection code is enabled automatically. The MCP23017 is detected during initialise, and if detected protection mode is automatically selected.

# Display

The original approach used an LCD display, interfaced via I2C. But adding standalone mode requires more “proper” displays and a 3.2” Nextion display is now used.

Interesting issue found: attempt to set the colour of display text using Set\_font\_color\_pco() resulted in the display or display library code crashing.

All displays have an enabled/disabled button and an ATU state string. This will change between “disabled”, “no tune”, Tuned” and “tuning”. Some displays will have an average/peak power button.

|  |  |
| --- | --- |
| Power bargraph display |  |
| Analogue power display |  |
| Crossed needle display  Probably quite flickery |  |
| Engineering display  This display is for debugging. You can see all the various “numbers” from the code. You can fine tune the ATU as well. |  |
| This page is displayed if the protection circuit detects a trip condition.  The 4 text boxes on the right display the tripped or not tripped state.  When all 4 are not tripped, the button (bottom right) test changes to Reset and when pressed the normal operation resumes. |  |

# Arduino Libraries

## Board support

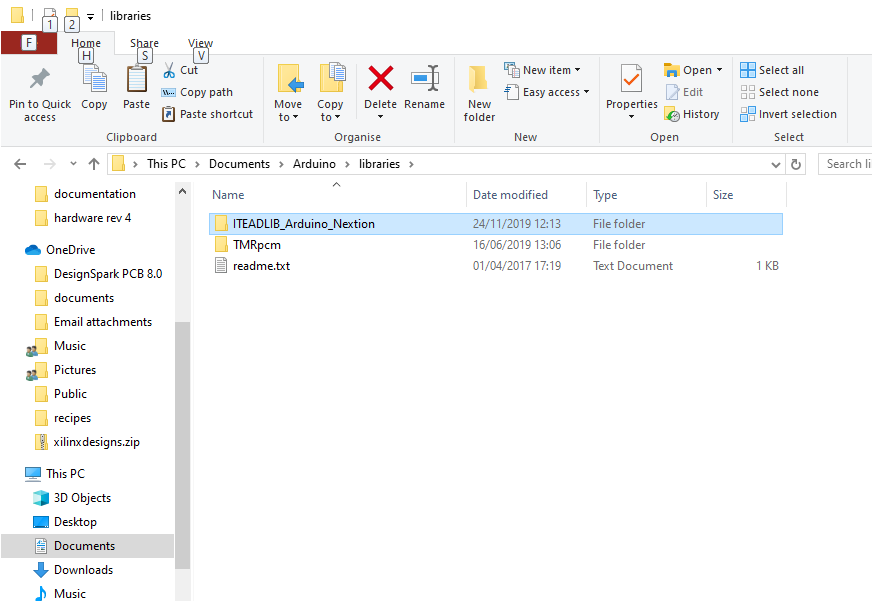
Install “Arduino SAMD boards (32 bit ARM Cortex-M0+) by Arduino”



## Nextion Library

There are issues using this library with an Arduino nano 33 IoT: any of the “read back” calls fail and hang the Arduino. There is an alternative library available that I’ve not yet tried that could fix that if required (https://github.com/jyberg/Enhanced-Nextion-Library)

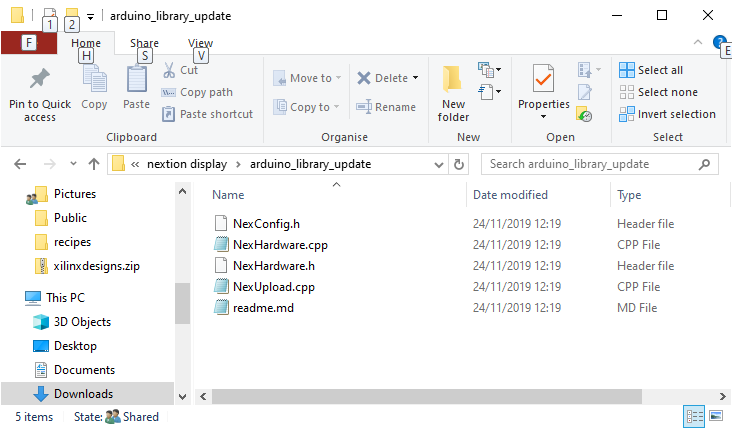
The library needs to be installed using a similar process:

1. Visit the repository on github: <https://github.com/itead/ITEADLIB_Arduino_Nextion>
2. Click “clone or download” then “download zip”
3. Store the zip file on your PC for example in the “downloads” folder
4. Open the zip file and extract all files. You will now have a folder “ITEADLIB\_Arduino\_Nextion-master” which will hold one folder also called “ITEADLIB\_Arduino\_Nextion-master”
5. Rename the second folder “ITEADLIB\_Arduino\_Nextion” (remove the “-master” part)
6. Copy that whole folder to your “documents\arduino\libraries” folder
7. (This is the library published by the display manufacturer. Be aware there is some foul language in the "html" folder - delete the entire "html" folder if you do not want that)
8. Your “documents\arduino\libraries” folder should now have that library:
9. 

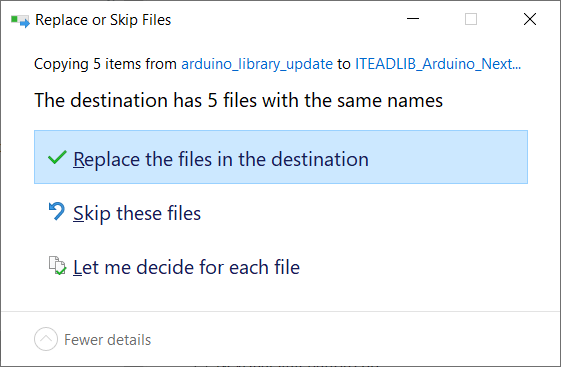
## Patch the ITEADLIB Library

Four files (plus a readme file) need to be copied from the Aries repository to the ITEADLIB folder in the Arduino libraries.

1. Open the folder “nextion display\arduino\_library\_update”
2. It will have files as follows:



1. Select then copy those files
2. Navigate to your folder "documents\arduino\libraries\ITEADLIB\_Arduino\_Nextion"
3. Paste the 5 files there. Make sure you replace the original files.

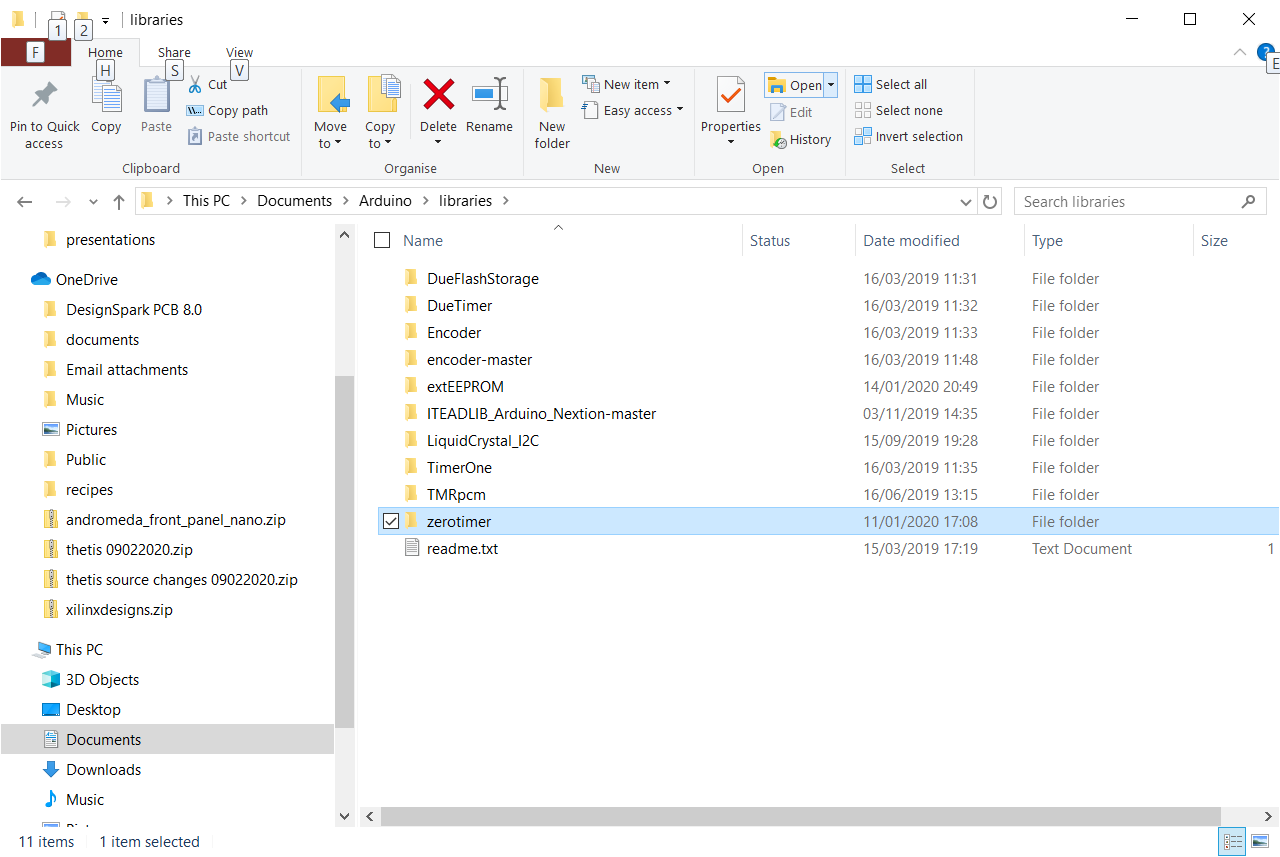


1. 4 existing files will be replaced and the readme file will be added.

## ZeroTimer Library

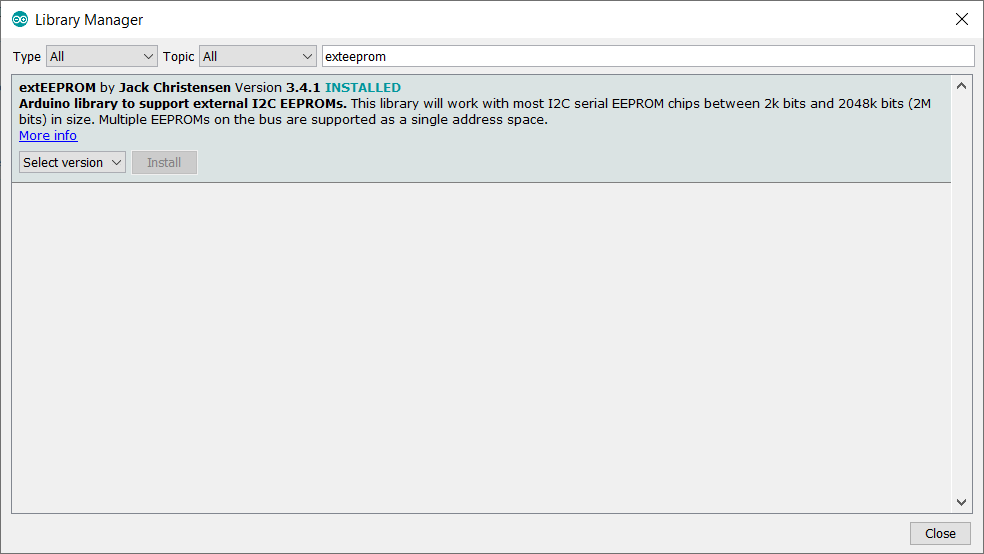
1. Download from <https://github.com/EHbtj/ZeroTimer>
2. Open the zip file and extract all files. You will now have a folder “ZeroTimer-master” which will hold one folder also called “ZeroTimer-master”
3. Rename the second folder “ZeroTimer” (remove the “-master” part)
4. Copy that whole folder to your “documents\arduino\libraries” folder

Your Arduino libraries folder will now include ZeroTimer:



## extEEPROM Library

1. click Tools > Manage Libraries…
2. The “installed libraries” form opens
3. Type “exteeprom” into the bar at the top
4. The library “extEEPROM” should be shown. Click Install
5. The library should show “installed”



## Compiling

Select Tools > Board > Arduino NANO 33 IoT

Click the “tick” icon to compile

Click the “right arrow” icon to download

There are some “conditional compile” options enabled using #define options in globalinclude.h

|  |  |
| --- | --- |
| #define SWVERSION nn  #define HWVERSION 5  #define PRODUCTID 2 | These are standard settings that can be displayed in Thetis. PRODUCTID must be set to 2.  It is important that rev 5 boards have HWVERSION set to 5. |
| #define VSWR\_SWAPVFVR | If defined, the Vf and Vr functions are swapped. This depends on the type of VSWR bridge. With rev 5 PCBs and binocular core, this should be commented out (//#define VSWR\_SWAPVFVR) |
| #define CONDITIONAL\_ALG\_DEBUG | If defined, additional debug messages are displayed to the serial console. This cannot be defined when connected to thetis! |
| #define CONDITIONAL\_ALG\_SIMVSWR | If defined, VSWR readings are simulated. This allows algorithm tests with no RF present. Must NOT be defined for normal use. |
| #define CONDITIONAL\_STREAM\_ADCREADINGS | If defined, Vf and Vr readings are streamed to the serial console. This is used for VSWR bridge debugging and must NOT be enabled in normal use. |
| #define VDISPLAYSCALE n  #define VNUMPAGES n | One less than the number of display scales available (when set to 4, 5 scales can be used)  The number of “normal” display pages available |

# To Do

Still to add to code:

* Add output drive to IC7.3 for High/Low Z select (done)
* On REV4 and earlier hardware, select D8 as output (done)
* On REV4 and earlier hardware, drive the High/Low output select to D8 (output) (done)
* On REV4 an earlier hardware, enter NORMAL mode on startup (done)
* On REV5 and newer hardware, select D8 as input (done)
* On REV5 and newer hardware, read the mode input from DIG12 on power up (done)
* Select I2C speed depending if standalone or not (400KHz if standalone, with additional MCP23017) (for now speed was already fixed at 400KHz)
* Initialise MCP23017 if standalone (done)
* Cycle the flip flop “reset” after power up (done)
* Add a “tripped” display (done)
* When “tripped” display the trip conditions (done)
* Monitor trip conditions, allow RESET operation when cleared (done)
* Monitor DIG8 every tick, and use to display TRIPPED page (done)
* Add BAND display legend (done)
* Get BAND inputs from MCP23017 in standalone mode and update if changed (done)
* Get BAND input from frequency in normal mode and update display if changed (done)
* PA Current display on engineering page (done)
* Initialise standalone antenna selection from h/w inputs at startup (done)
* Sort out the Nextion buttons not operating in engineering mode (done)
* Add code to disable Nextion display commands if not used (detected from NexInit return value) (done)
* Test all of the above!

# Rev 5 PCB Modifications

RL18 has the wrong PCB footprint. Wiring changes required:

A screenshot of a computer

Description automatically generated with medium confidence

There is a short to ground on signal “REV” on the top side of the board between R13 and L20 near J12 & F1. This is because the track is too close to a ground via. The track needs to be cut away slightly to remove the connection to the via.

